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| L6 | 2 | ("6160443").PN. | US-PGPUB; OR USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OFF | 2006/01/06 11:38 |
| L7 | 2 | ("6249179").PN. | US-PGPUB; OR USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OFF | 2006/01/06 11:39 |
| L8 | 2 | ("6249180").PN. | US-PGPUB; OR USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OFF | 2006/01/06 11:41 |
| L9 | 2 | ("6268767").PN. | US-PGPUB; OR USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OFF | 2006/01/06 12:26 |

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In English:

[0001] The numerical data transmission through a communication network is often carried out per packages. When the network is strictly transparent, it transmits the package as if it acted of an analogical signal, i.e. it does not fix each elementary period of bit of the package on a possible clock of the network. It is for example the case when they are simple cables, used, for example, in a network of diffusion at the request of programs of television, to transmit, at the station of head, the requests of the subscribers. This transmission can be carried out by using a temporal screen according to the technique ~~TDMA~~ (Multiple Access by Division of Time) in which one assigns a temporal way to each user. The transmissions can also relate to connections point-multipoint such as type LMDS (System of Multipoint Distribution Local)

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[0001] La transmission de données numériques à travers un réseau de communication s'effectue souvent par paquets. Lorsque le réseau est strictement transparent, il transmet le paquet comme s'il s'agissait d'un signal analogique, c'est-à-dire qu'il necale

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In English:

[0002] So a receiver must, in control circuits of sampling, to initially analyze the shape of any signal received to recognize that it acts of a package and to determine one precise moment for which it is necessary to start to sample the signal recurrently, at the given rate/rhythm of the emission of the bits and, more generally, of symbols. It is thus necessary to manage to reproduce the clock having been used for the emission of the received symbols, and this with a delay corresponding exactly to the transmission, so, on the one hand, carrying out a logical synchronization to restore all the symbols of the package by locating the position of the first, and only those, and, on the other hand, to carry out an analogical synchronization, making it possible to correctly read each logical state of received symbol. Analogical synchronization requires to fix in phase sampling on the clock of emission, by holding account of the time of transmission, so that each symbol, whose reception started, is sampled with a sufficient delay to allow its stabilization and thus to read it without error. Obviously, logical synchronization can intervene only if analogical synchronization is established.

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[0002] De ce fait, un récepteur doit, dans des circuits de commande d'échantillonnage, analyser d'abord la forme de tout signal reçu pour reconnaître qu'il s'agit d'un paquet et pour déterminer un instant précis auquel il faut commencer à échantillonner cycliquement



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In English:

[0003] It is thus known to seek analogical synchronization, or synchronization on the rate/rhythm of the symbols, then to seek logical synchronization then, of the package, according to a predetermined preamble, to locate the position of the first symbol and thus those of the following according to the rate/rhythm. However, if the packages are low length, their preamble is also length limited in order to avoid a proportionally excessive loss of time of transmission, so that there remains a risk of defect of synchronization symbol, and thus of nonlogical synchronization.

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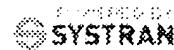
[0003] Il est ainsi connu de rechercher la synchronisation analogique, ou synchronisation sur le rythme des symboles, puis de rechercher ensuite la synchronisation logique, du paquet, d'après un préambule prédéterminé, pour repérer la position du

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In English:

[0004] the present invention aims at reducing such a risk of bad synchronization. [0005] To this end, the invention relates to a process of synchronization of a receiver of numerical signals of transmitted data, at determined intervals, by packages of data preceded by a predetermined preamble, in which one continuously carries out a treatment of search for probability relating to ~~a block of signals~~, comprising the preamble and the data of the remainder of the package, one memorizes the received signals and, when a maximum of probability is reached, one launches a sampling of reading, at the determined rate/rhythm, signals of data already received and memorized.

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[0004] La présente invention vise à réduire un tel risque de mauvaise synchronisation.

[0005] A cet effet, l'invention concerne un procédé de synchronisation d'un récepteur de signaux de données numériques transmis, à un

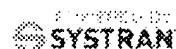
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In English:

[0006] Thus, the optimak moment to sample a first logical symbol of the package can be given with a high precision since the maximum of probability is established according to an analysis overflowing of the only preamble, to extend on the data field themselves. [0007] Advantageously, one works out a flow of values of power of the data of beginning of package and a flow of values of correlation between the predetermined preamble and the received preamble, by a continuous calculation of the current prices considered, and one delays the flow of values of correlation to add it with flow with values with power and thus to obtain a value of probability.

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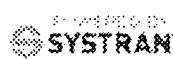
[0006] Ainsi, l'instant optimal pour échantillonner un premier symbole logique du paquet peut être déterminé avec une précision élevée puisque le maximum de vraisemblance est établi d'après une analyse débordant du seul préambule, pour s'étendre sur la zone de

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In English:

0008] the present invention will be better included/understood using the following description of a mode of implementation preferred of the process of the invention, in reference to the annexed drawing, on which: figure 1 is a diagram per blocks of a receiver for the implementation of the process of the invention, figure 2 represents, according to time T, a package of data in an interval of time of a screen of transmission, figure 3 illustrates more in details, in the form of building blocks, an arithmetic unit of power, and figure 4 illustrates a circuit of determination of a maximum value in a continuation.

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0008] La présente invention sera mieux comprise à l'aide de la description suivante d'un mode de mise en œuvre préféré du procédé de l'invention, en référence au dessin annexé, sur lequel :

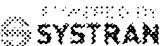
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In English:

[0009] The receiver of figure 1 belongs to a station network head of a network of diffusion at the request of programs of television and comprises a stage of entry 1, of filtrage/demodulation of a signal $y(t)$, coming from a coaxial cable of a network of transmission, for which a more detailed explanation is provided further. The signal received $y(t)$ corresponds to a succession of requests of subscribers, distributed in temporal ways of a screen TDMA, selecting a particular program. Each request consists of a ~~package R~~ of ~~symbols~~ with a preamble ~~predetermined~~ Pr of recognition and synchronization, of P . duration When an subscriber wants to transmit a request, it to him is allocated an interval of time IT , or sees temporal, in the temporal screen repeating itself recurrently, the interval of time IT having one duration equal to that of a package R increased by a margin M , or interval of guard, to tolerate fluctuations of the moment of emission of the package R and to take into account the time of transmission.

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[0009] Le récepteur de la figure 1 appartient à une station tête de réseau d'un réseau de diffusion à la demande de programmes de télévision et comporte un étage d'entrée 1, de filtrage/démodulation d'un signal $y(t)$, provenant d'un câble coaxial d'un réseau de

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In English:

[0010] The stage of entry 1 provides a signal $x(t)$ filtered in baseband to a delay circuit 2, like a delay line, which memorizes the signal over one duration $P + D$ and restores it, with the delay $P + D$, with a circuit 3 of sampling in reading, ordered by a circuit bases time 9 with ordered release. The duration D is that of a numerical data field Do, package R, following the preamble Pr here. [0011] The signal $x(t)$ is in addition applied in entry of a unit 10 of ordering of the circuit of sampling 3. The signal $x(t)$ is applied there, through a circuit 4 with delay D , with an arithmetic unit 5, calculating the correlation between the preamble predetermined Pr , and memorized locally, and a zone of beginning of received signal, to detect the preamble Pr there. [0012] An arithmetic unit 6 also receives, directly, the signal $x(t)$ to determine there the power of the signal $x(t)$ in the Do zone.

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[0010] L'étage d'entrée 1 fournit un signal $x(t)$ filtré en bande de base à un circuit à retard 2, comme une ligne à retard, qui mémorise le signal sur une durée $P + D$ et le restitue, avec le retard $P + D$, à un circuit 3 d'échantillonnage en lecture, commandé par

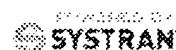
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In English:

[0013] Circuit 4, delay D, makes it possible to fix the respective moments of end of the calculation of correlation, duration P, and the calculation of power, of duration D, which is naturally delayed duration P since the data of the zone concerned Do are transmitted only after the preamble Pr, therefore with a delay P. [0014] An adder 7 provides the sum of the results of the arithmetic units 5 and 6 to a circuit 8 of detection of maximum ordering release of the base of time 9. Two terms, or flows, added can be balanced one compared to the other according to the parameters of the system of transmission. [0015] The detail of the operations of the circuits above now will be exposed. [0016] According to the process of the invention, one continuously carries out a treatment of search for probability relating to the block of signals of the package R, block comprising the preamble Pr and the Do data of the remainder of the package R, one memorizes the signals of Do data of the block and, when a maximum of probability is reached, one launches a sampling of reading, at the given rate/rhythm F, signals of already received and memorized Do data and, if necessary, those which follow.

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[0013] Le circuit 4, à retard D, permet de caler les instants respectifs de fin du calcul de corrélation, de durée P, et du calcul de puissance, de durée D, qui est naturellement retardé de la durée P puisque les données de la zone concernée Do ne sont

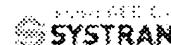
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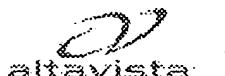
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In English:

[0017] In this example, one works out, in block 6, a flow of values of power of the Do data of beginning of package and, in block 5, a flow of values of correlation between the preamble predetermined Pr and the received preamble, by a continuous calculation of the current prices considered, the flow of values of correlation, being delayed D to add it with flow with values with power and thus to obtain a value of probability. [0018] As indicated higher, in this example the packages, of determined duration, are transmitted, without coordination between them, in the intervals of respective times IT of the temporal screen, intervals of time IT of duration corresponding to that of a package R increased by the margin Mr. One carries out a synchronization specific to each interval of time IT in a temporal window starting at the beginning of this one and including the margin M, i.e. one considers that the package R can be fixed at the end of the interval of time, the margin M being then entirely before the package R. [0019] The search for a maximum in circuit 8 is carried out continuously, i.e. the signal $x(t)$ runs out in the various circuits and it is analyzed throughout this flow. For a digital processing, the samples of the signal $x(t)$ are quantified on a given number of bits, for example 10 bits.

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[0017] Dans cet exemple, on élabore, dans le bloc 6, un flux de valeurs de puissance des données Do de début de paquet et, dans le bloc 5, un flux de valeurs de corrélation entre le préambule prédeterminé Pr et le préambule reçu, par un calcul continu des

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In English:

[0020] In this example, the analysis is carried out in a numerical way in a microprocessor DSP, or a ASIC or programmable logical circuit, so well the signal $x(t)$ is sampled here, for analysis, at a frequency of F_s oversampling higher than its frequency F of transmission, for example $K =$ eight times this frequency F . The eight samples taken over one period T of a transmitted symbol then represent sufficiently accurately the received signal, an interpolation being moreover possible. The delay circuits 2 and 4 can thus be numerical, containing integrated memories. It is not besides necessary that the frequency of F_s oversampling is a multiple entirely of the frequency of emission of the symbols, i.e. unit 10 can have a frequency of autonomous operation, only the base of time 9 being related to the frequency of emission F . [0021] The determination of correlation of block 5 conforms to the following equation: $EMI6.1$ with I : row of the symbol in the package, here in the S^i preamble: combined complex representation of the reason for symbols of predetermined preamble. LP : a number of symbols of the preamble, here = 16. T : current moment.

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[0020] Dans cet exemple, l'analyse est effectuée de façon numérique dans un microprocesseur DSP, ou un ASIC ou circuit logique programmable, si bien le signal $x(t)$ est ici échantillonné, pour analyse, à une fréquence de suréchantillonnage F_s supérieure



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In English:

[0022] Like higher exposed, there are in fact eight times more stages of calculation than reveals it the formula above, since there are here eight samples per symbol of duration T. [0023] The calculation of power in block 6 answers the equation: EMI6.2 with L (here: 252): a number of symbols of the preamble Pr and the zone C, this calculation thus relating only to this zone. [0024] One will note that calculations of blocks 5 and 6 can, in alternative, to relate to formulas as indicated but with an exhibitor whom can be other than 2, and for example being worth 1, the two choices of the values of exhibitor being independent from one formula to another, but preferably equal. In a general way, any increasing function according to the module of the variable is appropriate.

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[0022] Comme exposé plus haut, il y a en fait huit fois plus d'étapes de calcul que ne le fait apparaître la formule ci-dessus, puisqu'il y a ici huit échantillons par symbole de durée T.

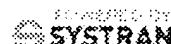
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In English:

[0025] interval 1 at L represents a temporal window of analysis of the signal received $x(t)$, ~~window~~ which this one crosses gradually as time passes. In other words, the ~~window of analysis~~ slips during one larger time of time ($M + L$), in which the beginning of a package is awaited. [0026] Figure 3 illustrates the logic design of block 6 of calculation of power. As it is a question of recurrently calculating, at the frequency of ~~oversampling~~ F_s , a sum slipping on a plurality of terms, kind $If = x_0 + x_1 + \dots + x_{n-1}$ then, the following time: $If + 1 = x_1 + \dots + x_{n-1} + x_n$ one calculates simply $If + 1 = If - x_0 + x_n$ thus in two operations of correction of the preceding value If . [0027] Thus, one calculates the new current price recurrently $If + 1$ of power in the temporal window of given size L which the signals $x(t)$ cross, by memorizing the preceding value beforehand If and by correcting it according to the difference $x_n - x_0$ powers of the two sections of signals respectively entered and left the ~~window~~ L of a calculation to the following.

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[0025] L'intervalle 1 à L représente une fenêtre temporelle d'analyse du signal reçu $x(t)$, fenêtre que traverse progressivement celui-ci à mesure que s'écoule le temps. En d'autres termes, la fenêtre d'analyse glisse dans une période de temps plus grande ($M + L$).

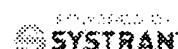
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In English:

[0028] To this end, the entering signal is applied to an arithmetic unit 61 carrying out the addition above (If) and is also applied, through a circuit 62 with delay corresponding to the ~~window~~ of analysis, with an arithmetic unit 63 similar to block 61. A subtracter 64, supplied with circuits 61 and 63, provides the value $x_n - x_0$ to an adder 65 whose exit, which is that of block 6, is rebouclée in entry through a line of $K =$ eight shift registers 66 advancing at the frequency of F_s oversampling, thus corresponding to a total delay of one period T of symbol. Eight registers 66 make it possible to interlace K temporally = eight series of calculations, carrying each one on a specific phase of the signal analyzed during the time T Ainsi, one carries out calculations of power per step at the frequency of multiple F_s oversampling-(K) of the rate/rhythm of transmission F , by recurrently affecting, with each calculation, a temporal section during the time of the rate/rhythm T , and one restores the preceding value If by delaying it of a number of step equal to multiple K and representing on the whole the period T Ainsi, adder 65 receives, to calculate If + 1, the signal If delayed T and the correction $x_n - x_0$

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[0028] A cet effet, le signal entrant est appliqué à un bloc de calcul 61 effectuant l'addition ci-dessus (Si) et est aussi appliqué, à travers un circuit 62 à retard correspondant à la fenêtre d'analyse, à un bloc de calcul 63 semblable au bloc 61. Un



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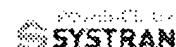
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In English:

wanted. [0029] In circuit 8, one detects the maximum of probability by cyclic comparisons between the current value and the preceding value of the probability provided by adder 7, for each time selecting the highest value and memorizing it, and one detects the maximum by the fact that the preceding value is again selected. [0030] More precisely, detector 8 of maximum, or peak, comprises a comparator of entry 81 and one multiplexer 82 to two ways receiving both, at the frequency of oversampling F_s , the new result of adder 7 and the preceding result coming from a delay circuit 83, with delay equal to the period of the frequency of F_s oversampling, is $T/8$ here. [0031] The exit of comparator 81 orders the shunting of multiplexer 82 to apply, with the entry of the delay circuit 83, the new result, current, as long as grows the analyzed signal coming from adder 7, and, if not, comparator 81 changes state to confirm the old result.

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[0029] Dans le circuit 8, on détecte le maximum de vraisemblance par des comparaisons cycliques entre la valeur actuelle et la valeur précédente de la vraisemblance fournie



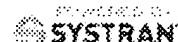
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In English:

[0032] Circuit 83 could be a register plug with memorizing ordered by a clock at F_s frequency, in which case multiplexer 82 could be replaced by a door of locking of the clock, the input signal from adder 7 arriving directly at the input of the register. The circuits above can with the need to treat numbers of more than ten bits, if the desired precision requires it.

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[0032] Le circuit 83 pourrait être un registre tampon à mémorisation commandée par une horloge à fréquence F_s , auquel cas le multiplexeur 82 pourrait être remplacé par une porte de verrouillage de l'horloge, le signal d'entrée issu de l'additionneur 7



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In English:

[0033] A maximum is then detected when the memorized provisional maximum (83) is rebouclé (82) first once (milked reinforced), the exit of comparator 81 indicating a refusal of the current signal and thus a moment of beginning of repercussion of the signal summons, making it possible to launch the base of time 9. The search for maximum is continued here on all the window of time in order to obtain the total maximum. In the absence of useful signals, one eliminates from the parasitic maximas, due to noise, the fact that they do not exceed a minimum threshold. As the signal received $x(t)$ is temporarily memorized in the delay circuit 2, this memorizing authorizes, without loss of the received signal, the prolongation of the analysis as a whole 10 beyond the moment to which one is detected (first) maximum. To delimit to better the total maximum, one can envisage to carry out the research of the maximum of probability on the totality of the package and possibly beyond, it be-with-statement which the Do zone represents then the totality of the package, except the preamble Pr, with, in end, a possible final section free from signals.

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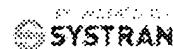
[0033] Un maximum est alors détecté lorsque le maximum provisoire mémorisé (83) est rebouclé (82) une première fois (trait renforcé), la sortie du comparateur 81 indiquant un refus du signal actuel et donc un instant de début de retombée du signal  

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In English:

[0034] One thus works out the maximum of probability from suréchantillons whose flow contains, subjacent, information of rate/rhythm of the symbols and position of the first symbol to be detected, information which is used simultaneously and not sequentially as in art former mentioned to the beginning. Obtaining the maximum of probability provides then, in a total continuous stage, simultaneously, in form clarifies, here numerical, the phase symbol and the position of the first signal useful to sample. Thus, one deduces directly, according to the moment to which occurs the maximum of probability, at the same time the phase of the signals received at the known as rate/rhythm and the temporal position of the data in those.

[0035] The base of time 9 comprises an oscillator at period definitely lower than T, for example of frequency F_s , but then multiple entirety of the frequency F of emission of the symbols of $x(t)$.

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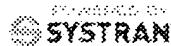
[0034] On élaboré ainsi le maximum de vraisemblance à partir des suréchantillons dont le flux contient, sous-jacentes, les informations de rythme des symboles et de position du premier symbole à détecter, informations qui sont utilisées simultanément

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In English:

[0036] When circuit 8 orders the launching of the base of time 9, a meter of this one, receiving the clock at F_s frequency, is then released and counts here on a eight time cycle, from 0 to 7, therefore over one duration T the state "0" being here of unspecified duration, taking into account the computing time in circuit 8 and of the possible absence of synchronization of the clock of the base of time 9 on the oversampling and computation channels 5 and 6, it is the state "1" which is decoded to provide an impulse of ordering of sampling to the circuit [0037] In this example, one memorizes the instantaneous value then presents at exit of the delay circuit 2, i.e. circuit 3 is here a shift register with entry of clock connected on the outlet side of a decoder of the state "1" of the meter of the base of time 9.

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[0036] Lorsque le circuit 8 commande le lancement de la base de temps 9, un compteur de celle-ci, recevant l'horloge à fréquence F_s , est alors libéré et compte ici sur un cycle de huit temps, de 0 à 7, donc sur une durée T . L'état "0" étant ici de durée

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In English:

[0038] If the delay circuit 2 would be carried out in the form of a memory RAM managed out of ring FIFO at Fs frequency, having at least the capacity for the wanted delay, it would have then to be addressed to choose the delay desired symbol (position of the first symbol then following) and more precisely to choose that of the eight suréchantillons having, during the time T, the optimal phase to read the symbol, i.e. a suréchantillon of level representing perfectly the emitted symbol.

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[0038] Dans le cas où le circuit à retard 2 serait réalisé sous forme d'une mémoire RAM gérée en anneau FIFO à fréquence F_s , ayant au moins la capacité pour le retard voulu, il faudrait alors l'adresser pour choisir le retard symbole voulu (position du premier

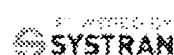
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In English:

[0039] In all the cases, the signal $x(t)$ is memorized, in analogical form or numerical form after digitalization by an oversampling making it possible to restore the form of it. One thus stores the total form, continues, of the signal $x(t)$ received, without seeking to detect there the symbols transmitted to frequency F , and it is only after the treatments above that one carries out sampling corresponding to the frequency F . As the signal $x(t)$ is memorized, time does not play more directly and one conceives that the concept of sampling at frequency F then includes the concept of space sampling in the memory, among suréchantillons, at one unspecified moment compared to the remainder of the data of the package R , which, with the choice, can be sampled as they arrive of circuit 1, without delay (2). [0040] In a case where each symbol would be represented by an evolutionary signal not in baseband, it would then be advisable to locate in circuit 2, according to the ordering of the base of time 9, the eights suréchantillons of each symbol to transmit them grouped to the circuit of sampling 3, at end of analysis, by demodulation to recognize the symbol

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[0039] Dans tous les cas, le signal $x(t)$ est mémorisé, sous forme analogique ou sous forme numérique après numérisation par un suréchantillonnage permettant d'en restituer la forme. On stocke donc la forme totale, continue, du signal $x(t)$ reçu, sans chercher

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